

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kiran V. Chatty et al.

Application No.: 10/709,041

Confirmation No.: 3040

Filed: April 8, 2004

Art Unit: 2815

For: LOW TRIGGER VOLTAGE ESD NMOSFET
TRIPLE-WELL CMOS DEVICES

Examiner: M. E. Warren

RESPONSE UNDER 37 C.F.R. 1.116 (FINAL OFFICE ACTION)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Madam:

INTRODUCTORY COMMENTS

This is in response to the final Office Action mailed May 28, 2009, in the above-captioned case.

Amendments to the Claims are reflected in the Listing of Claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.

	Claims remaining after Amendment		Highest number previously paid for		Present extra
Total	17		20		0
Independent	3		3		0